

3.5 Ω , 400MHz Bandwidth, Dual SPDT Analog Switch

Features

- Wide Power Supply Range: 1.8V to 5.5V
- On-Resistance: 3.5 Ω (TYP) at 5.0V
- Low On-Resistance Flatness
- High Bandwidth: 400MHz
- Break-Before-Make Switching
- Rail-to-Rail Signal Range
- High Off-Isolation: -75dB (f=1MHz)
- Crosstalk Rejection: -98dB (f=1MHz)
- Operation Temperature Range:
-40°C to +125°C
- Available in QFN1.8x1.4-10L package

Applications

- Wearable Devices
- Portable Instrumentation
- Battery-Operated Equipment
- Computer Peripherals
- Cell Phones

Function Table

IN _x	Function
0	NC _x Connected to COM _x
1	NO _x Connected to COM _x

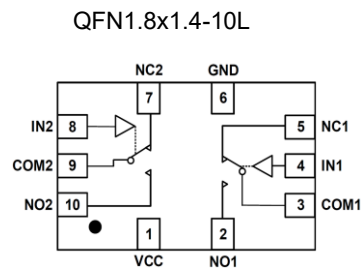
Description

The BL1557 is a dual, single-pole double-throw (SPDT) analog switch that is designed to operate from 1.8 V to 5.5 V.

The BL1557 can handle both analog and digital signals. It features high-bandwidth(400MHz) and low on-resistance (3.5Ω TYP).

The BL1557 is available in QFN1.8x1.4-10L package

Pin Configuration



Pin Description

Pin Name	Type	Description
VCC	PWR	Power Supply
GND	Ground	Ground
COM _x	Input/Output	Data Port
NC _x	Input/Output	Data Port
NO _x	Input/Output	Data Port
IN _x	Input	Logic Control Signal

** X = 1 or 2

Ordering Information

Order No.	Package	Packing
BL1557QN	QFN1.8×1.4-10L	Tape and Reel, 3000

ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Min	Max	Units
DC Supply Voltage	V_{CC}	-0.5	6	V
DC Switch Voltage	$V_{NCX}/V_{NOX}/V_{COMX}$	-0.5	$V_{SUP} + 0.3$	V
DC Input Voltage	V_{INX}	-0.5	6	V
Continuous Current	$I_{(NCX/NOX/COMX)}$	-200	+200	mA
Peak Current ⁽¹⁾	$I_{PEAK(NCX/NOX/COMX)}$	-300	+300	mA
Storage Temperature Range	T_{STG}	-65	150	°C

Notes:

- (1) Pulsed at 1ms, 50% duty circle
- (2) Stress beyond above listed "Absolute Maximum Ratings" may lead permanent damage to the device.
 These are stress ratings only and operations of the device at these or any other conditions beyond those indicated in the operational sections of the specifications are not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.
- (3) Control input(V_{INX}) must be held HIGH or LOW, and mustn't be floated.

RECOMMENDED OPERATING CONDITIONS

DC Supply Voltage (V_{CC})	1.8V to 5.5V
Switch Input Voltage (V_S)	0V to V_{CC}
Control Input Voltage (V_{IN})	0V to V_{CC}
Operation Temperature (T_A)	-40°C to +125°C

DC ELECTRICAL CHARACTERISTICS

 ($V_{CC}=5V$, $T_A=-40^{\circ}C$ to $+125^{\circ}C$. Typical values are at $V_{CC}=+25^{\circ}C$, unless otherwise noted)

PARAMETER	SYMBOL	CONDITIONS	TA	MIN	TYP	MAX	UNITS
ANALOG SWITCH							
Analog Signal Range	V_{NO}, V_{NC}, V_{COM}		$-40\sim+125^{\circ}C$	0		VCC	V
On-Resistance	R_{ON}	$V_{CC}=5V, I_{COM} = -10mA, V_{NO}$ or $V_{NC}=3.5V$	$+25^{\circ}C$		3.5	7.5	Ω
			$-40\sim+125^{\circ}C$			8	
On-Resistance Match Between Channels ⁽¹⁾	ΔR_{ON}	$V_{CC}=5V, I_{COM} = -10mA, V_{NO}$ or $V_{NC}=3.5V$	$+25^{\circ}C$		0.15	0.5	Ω
			$-40\sim+125^{\circ}C$			0.7	
On-Resistance Flatness ⁽²⁾	$R_{FLAT(ON)}$	$V_{CC}=5V, I_{COM} = -10mA, V_{NO}$ or $V_{NC}=0\sim V_{CC}$	$+25^{\circ}C$		1.2	2	Ω
			$-40\sim+125^{\circ}C$			2.2	
NC or NO OFF Leakage Current	$I_{NC(OFF)}, I_{NOFF}$	$V_{CC} = 5.5V; V_{NO}$ or $V_{NC} = 3.3V/0.3V; V_{COM} = 0.3V/3.3V$	$+25^{\circ}C$		0.01	0.1	μA
			$-40\sim+125^{\circ}C$			1	
NC,NO,COM ON Leakage Current	$I_{NC(ON)}, I_{NO(ON)}, I_{COM(ON)}$	$V_{CC} = 5.5V; V_{COM} = 0.3V/3.3V; V_{NO}$ or $V_{NC} = 0.3V/3.3V$, or floating	$+25^{\circ}C$		0.01	0.1	μA
			$-40\sim+125^{\circ}C$			1	
DIGITAL CONTROL INPUTS							
Input High Voltage	V_{IH}	$V_{CC}=5.5V$	$-40\sim+125^{\circ}C$	1.7			V
Input Low Voltage	V_{IL}	$V_{CC}=5.5V$	$-40\sim+125^{\circ}C$			0.6	V
Input Leakage Current	I_{IN}	$V_{CC}=5.5V, V_{IN} = 0$ or $5.5V$	$-40\sim+125^{\circ}C$		0.01	1	μA
DYNAMIC CHARACTERISTICS							
Turn-On Time	t_{ON}	V_{NO} or $V_{NC} = 3.0V, R_L = 50\Omega; C_L = 35pF, V_{IH}=1.5V, V_{IL}=0V$ (50% to 50%)	$+25^{\circ}C$		20		ns
Turn-Off Time	t_{OFF}	V_{NO} or $V_{NC} = 3.0V, R_L = 50\Omega; C_L = 35pF, V_{IH}=1.5V, V_{IL}=0V$ (50% to 50%)	$+25^{\circ}C$		15		ns
Break-Before-Make Time Delay	t_{BBM}	V_{A1} or $V_{A2} = 3.0V, R_L = 50\Omega; C_L = 35pF(90\% \text{ to } 90\%)$	$+25^{\circ}C$		12		ns
-3dB Bandwidth	BW	Signal = 0dBm, $R_L = 50\Omega, C_L = 5pF$	$+25^{\circ}C$		400		MHz
Off Isolation ⁽³⁾	O_{ISO}	$R_L = 50\Omega, C_L = 5pF, \text{Signal} = 0dBm$	$f = 1MHz$	$+25^{\circ}C$		-75	dB
			$f = 10MHz$	$+25^{\circ}C$		-55	dB

Crosstalk ⁽⁴⁾	X _{TALK}	RL = 50 Ω, CL=5pF	f = 1MHz	+25°C		-98		dB
			f = 10MHz	+25°C		-85		dB
NC,NO OFF Capacitance	C _{NC(OFF)} , C _{NO(OFF)}			+25°C		6		pF
NC,NO,COM ON Capacitance	C _{NC(ON)} , C _{NO(ON)} , C _{COM(ON)}			+25°C		18		pF
Charge Injection	Q	CL = 1.0nF, VG = 0V, RG = 0Ω		+25°C		45		pC
POWER REQUIREMENTS								
Power Supply Current	I _{CC}			-40~+125°C		0.01	1	uA

DC ELECTRICAL CHARACTERISTICS

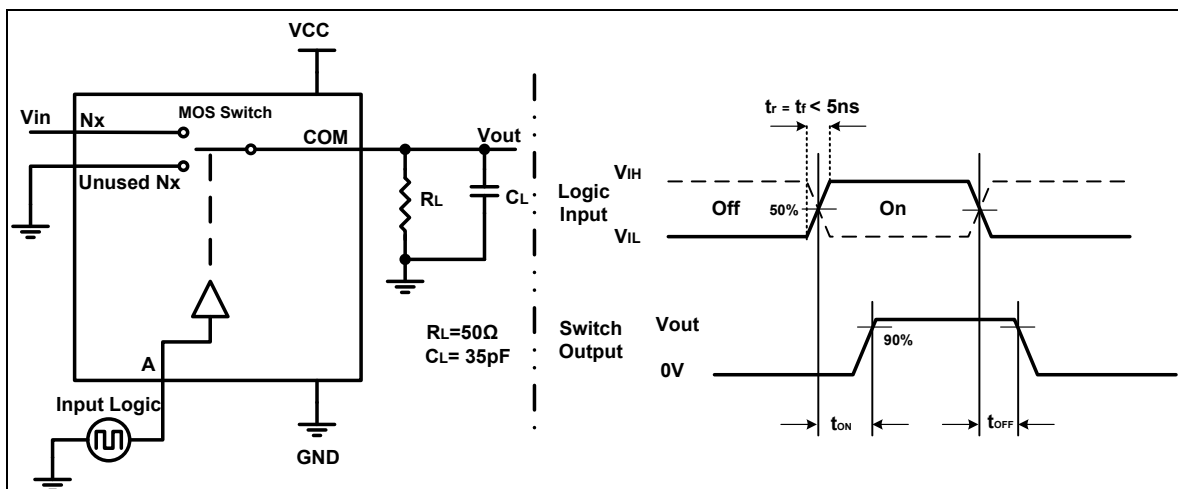
(V_{CC}=2.7V, T_A=-40°C to +125°C. Typical values are at V_{CC}= +25°C, unless otherwise noted)

PARAMETER	SYMBOL	CONDITIONS	TA	MIN	TYP	MAX	UNITS
ANALOG SWITCH							
Analog Signal Range	V _{NO} , V _{NC} , V _{COM}		-40~+125°C	0		V _{CC}	V
On-Resistance	R _{ON}	V _{CC} =2.7V, I _{COM} = -10mA, V _{NO} or V _{NC} =1.5V	+25°C		8.8	12	Ω
			-40~+125°C			15	
On-Resistance Match Between Channels ⁽¹⁾	ΔR _{ON}	V _{CC} =2.7V, I _{COM} = -10mA, V _{NO} or V _{NC} =1.5V	+25°C		0.1	0.5	Ω
			-40~+125°C			0.7	
On-Resistance Flatness ⁽²⁾	R _{FLAT(ON)}	V _{CC} =2.7V, I _{COM} = -10mA, V _{NO} or V _{NC} =0~V _{CC}	+25°C		4.4	6	Ω
			-40~+125°C			7	
NC or NO OFF Leakage Current	I _{NC(OFF)} , I _{NO(OFF)}	V _{CC} = 3.6V; V _{NO} or V _{NC} = 0.3V, 3.3V; V _{COM} = 0.3V, 3.3 V	+25°C		0.01	0.1	μA
			-40~+125°C			1	
NC,NO,COM ON Leakage Current	I _{NC(ON)} , I _{NO(ON)} , I _{COM(ON)}	V _{CC} = 3.6V; V _{COM} = 0.3V, 3.3 V; V _{NO} or V _{NC} = 0.3V, 3.3V, or floating	+25°C		0.01	0.1	μA
			-40~+125°C			1	
DIGITAL CONTROL INPUTS(1)							
Input High Voltage	V _{IH}	V _{IN} = 3.6V	-40~+125°C	1.5			V
Input Low Voltage	V _{IL}	V _{IN} = 3.6V	-40~+125°C			0.5	V
Input Leakage Current	I _{IN}	V _{IN} = 0 ~ 3.6V	-40~+125°C			1	μA
DYNAMIC CHARACTERISTICS							
Turn-On Time	t _{ON}	V _{NO} or V _{NC} = 1.5V, R _L = 50Ω; C _L = 35pF, V _{IH} =1.5V, V _{IL} =0V	+25°C		23		ns

Turn-Off Time	t_{OFF}	V_{NO} or $V_{NC} = 1.5V$, $R_L = 50\Omega$; $CL = 35pF$, $V_{IH}=1.5V, V_{IL}=0V$	+25°C		21		ns
Break-Before-Make Time Delay	t_{BBM}	V_{NO} or $V_{NC} = 1.5V$, $R_L = 50\Omega$; $CL = 35pF$	+25°C		18		ns
-3dB Bandwidth	BW	Signal = 0dBm, $R_L = 50\Omega$, $CL = 5pF$	+25°C		400		MHz
Off Isolation ⁽³⁾	O_{ISO}	$R_L = 50\Omega$, $CL = 5pF$, Signal = 0dBm	$f = 1MHz$	+25°C		-73	dB
			$f = 10MHz$	+25°C		-50	dB
Crosstalk ⁽⁴⁾	X_{TALK}	$R_L = 50\Omega$, $CL=5pF$	$f = 1MHz$	+25°C		-100	dB
			$f = 10MHz$	+25°C		-85	dB
NC,NO OFF Capacitance	$C_{NC(OFF)}$, $C_{NO(OFF)}$		+25°C		6		pF
NC,NO,COM ON Capacitance	$C_{NC(ON)}$, $C_{NO(ON)}$, $C_{COM(ON)}$		+25°C		18		pF
Charge Injection	Q	$CL = 1.0nF$, $V_G = 0V$, $R_G = 0\Omega$,	+25°C		30		pC
POWER REQUIREMENTS							
Power Supply Current	I_{CC}		-40~+125°C		0.01	1	uA

Note:

- (1) $\Delta R_{ON} = R_{ON(MAX)} - R_{ON(MIN)}$, between channels
- (2) Flatness is defined as the difference between the maximum and minimum value of on resistance as measured over the specified analog signal ranges.
- (3) Off Channel Isolation = $20\log_{10} [(V_{NOINC})/V_{COM}]$
- (4) Between any two switches

TEST SETUP CIRCUITS

Figure1. AC Test Circuit & Waveforms

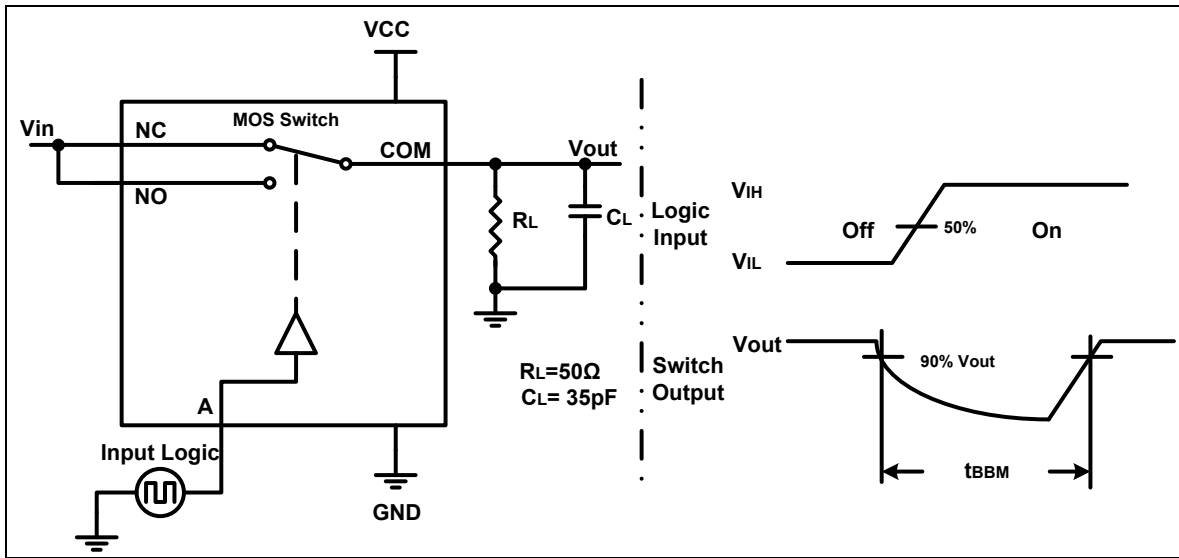


Figure2. Break-Before-Make Time (t_{BBM})

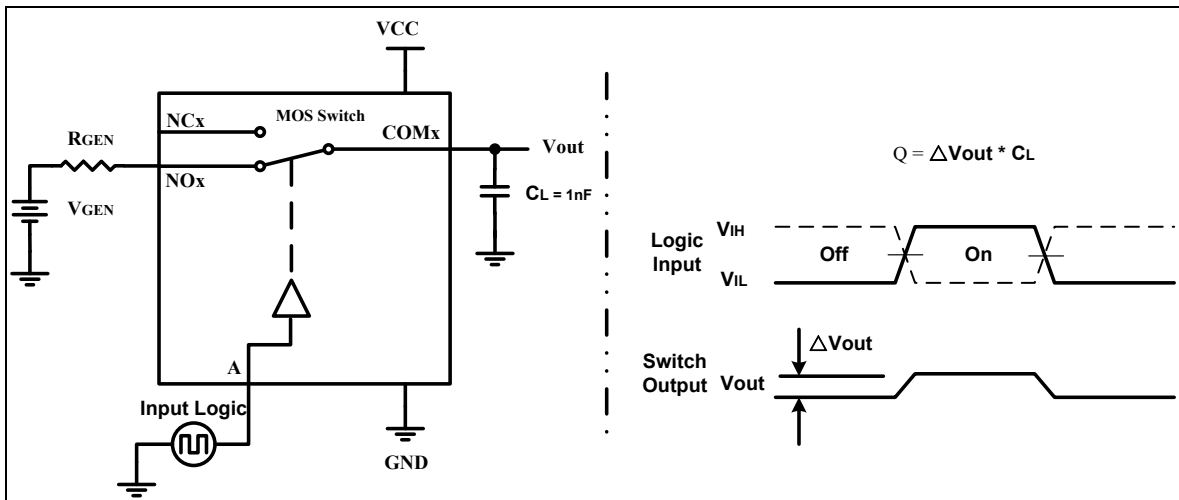


Figure3. Charge Injection (Q)

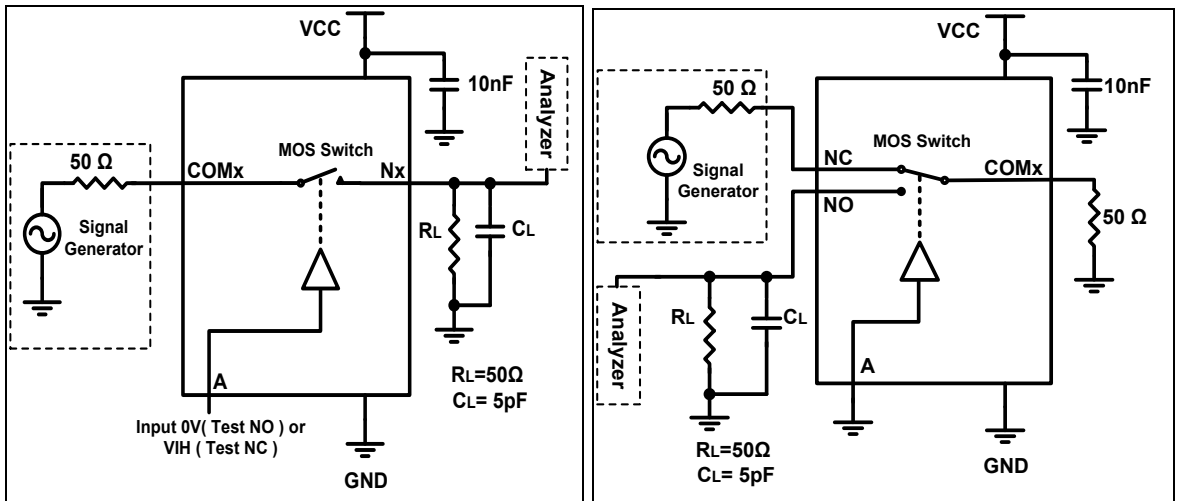


Figure4. Off Isolation (V_{ISO})

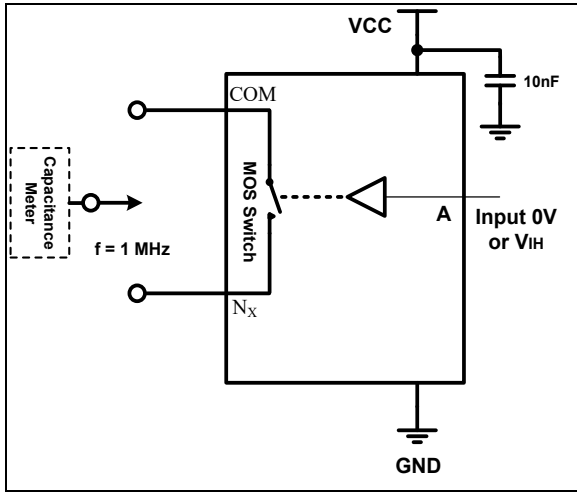


Figure5. Cross Talk (V_{CT})

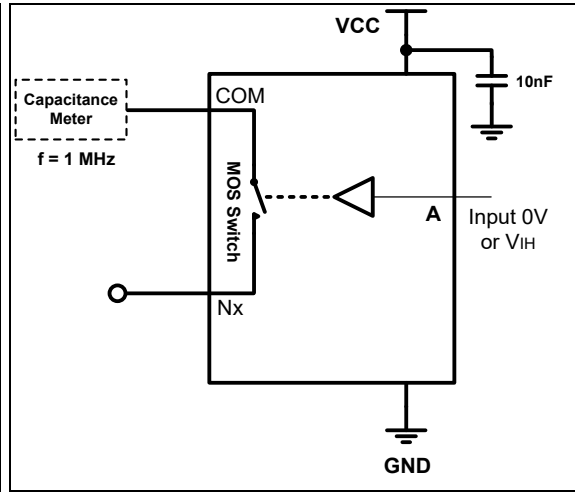


Figure6. Channel Off Capacitance($C_{OFF(NX)}$) Figure7. Channel On Capacitance($C_{ON(NX)}$)

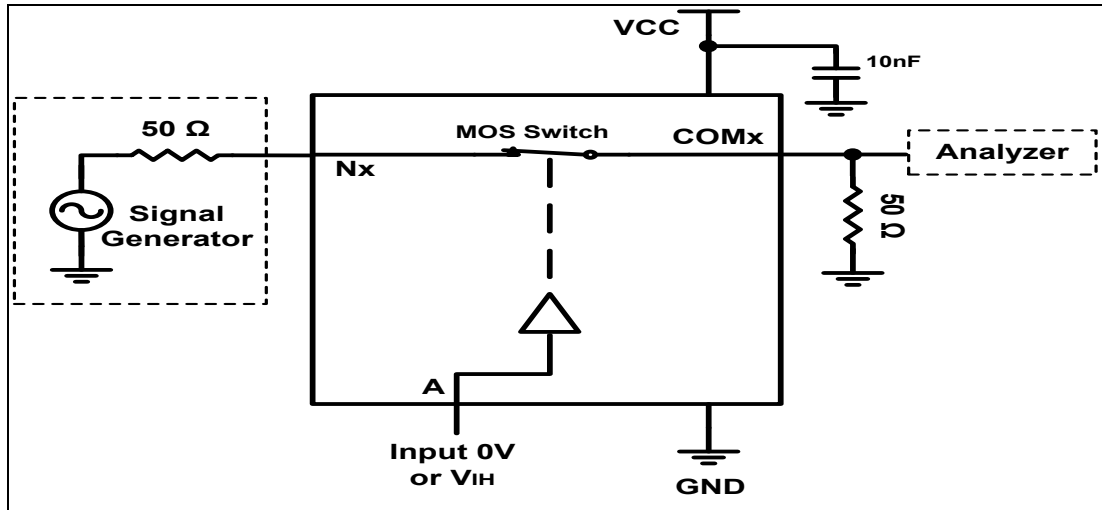
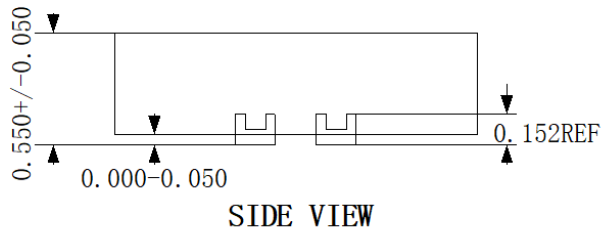
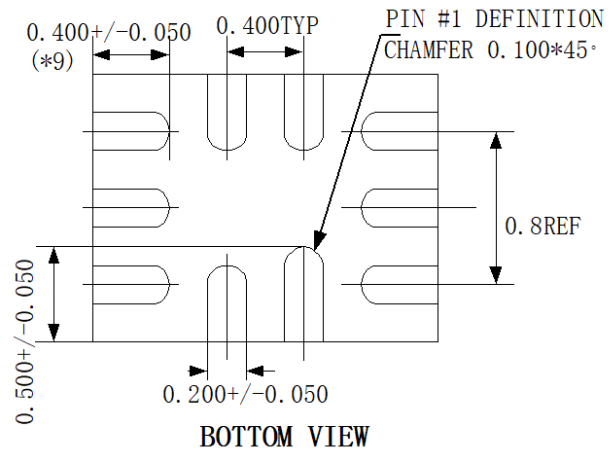
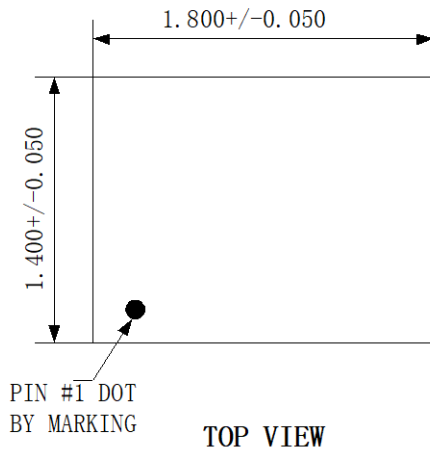


Figure8. -3dB Bandwidth (f_{3dB})

PACKAGE OUTLINE DIMENSIONS

QFN1.8×1.4-10L



NOTE: All linear dimensions are in millimeters.